



JFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

H. ISHIDA et al.

Art Unit: 2816

Application No.: 10/613,123

Examiner: J. Zweizig

Filed: July 7, 2003

Attorney Dkt. No.: 100021-00128

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE ENABLING TO PRODUCE A STABLE CONSTANT CURRENT EVEN ON A LOW POWER-SOURCE VOLTAGE

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 2, 2004

Sir:

In response to the Election/Restriction Requirement mailed August 16, 2004, the Applicants hereby elect Group I, directed to claims 1-8. Applicants reserve the right to file one or more divisional applications on the non-elected claims.

In view of the Applicants' above election, Applicants respectfully submit that the Restriction Requirement has been satisfied. Accordingly, Applicants respectfully request examination of elected claims 1-8 on the merits.

Please charge any fee deficiency or credit any overpayment to Deposit Account No. 01-2300, referring to client-matter number 100021-00128.

Respectfully submitted,

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